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EPICS-FPGA Interface Implementation (Part B)

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Part B of EPICS to FPGA interfacing where current implementations of local and remote EPICS interfaces are explored. These are in the form of embedded processor and FPGA technologies which are on the pathway to a future of converged EPICS-FPGA interfaces.

Primary author(s) : HOGAN, Ross (Australian Synchrotron - ANSTO)

Co-author(s) : CHEN, Simin (ANSTO)

Presenter(s) : HOGAN, Ross (Australian Synchrotron - ANSTO)

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