EPICS Collaboration Meeting



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EPICS on Red Pitaya

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Red Pitaya is a small board equipped with Xilinx Zynq SOC and Cortex A9. It has 2 14-bit, 125 MS/s RF inputs and outputs as well as slow analogue and digital inputs and outputs.

We've written an asyn port driver fir it and it is currently used in a few applications across the Australian Synchrotron without any problems.

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