

Contribution ID : 222 Type : Poster

# Using low energy ion beams to pattern the surface of novel semiconductors

Thursday, 25 November 2021 18:12 (1)

A wide range of ion energies (KeV- MeV), ion species, and ion fluences achievable by ion beam implantation, which allows fabrication of highly customized patterned subsurface structures in materials. This advanced material processing technology allows tuning of specific magnetic, and electronic properties with the aim of achieving a wide range of functionalities in electronics. Magnetic ions implantation has been actively used for functionalising semiconductor materials in recent few years in attempt to fabricate magnetic semiconductors for spintronic applications. [1, 2] Ion beam patterning, like electron-beam lithography, able to fabricate customised geometryies on a surface of a semiconductor to create a functionalised region with desired electronic and magnetic properties. By using low energy ion beam implanter at Center of Accelerator Science (CAS) at Australian Nueclear Science and Technology Organisation (ANSTO), we demonstrate that the current method has the potential application in the integrated circuitry processing industry with the ability to "write" very small features down to few tens of nanometers.

## Level of Expertise

Student

#### **Presenter Gender**

Man

#### **Pronouns**

### Which facility did you use for your research

Centre for Accelerator Science

Students Only - Are you interested in AINSE student funding

Yes

Do you wish to take part in the Student Poster Slam

Yes

#### Condition of submission

**Primary author(s):** BAKE, Abuduliken (University of Wollongong); PASTUOVIC, Zeljko (Centre for Accelerator Science of ANSTO); Dr EVANS, Peter (ANSTO); Dr NANCARROW, Mitchell (University of Wollongong); CORTIE, David (The University of Wollongong)

Presenter(s): BAKE, Abuduliken (University of Wollongong)

**Session Classification :** Poster Session

Track Classification: Physics, Surface & Condensed Matter